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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/715,311	1	11/17/2003	Brian J. Campbell	BP2414CON	5109
34399	7590	05/06/2004	EXAMINER		INER
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AUSTIN, TX 78716-0727				ART UNIT	PAPER NUMBER
				2818	

DATE MAILED: 05/06/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

·	Application No.	Applicant(s)				
	10/715,311	CAMPBELL ET AL.				
Office Action Summary	Examiner	Art Unit				
	Son L. Mai	2818				
The MAILING DATE of this communication ap	pears on the cover sheet with the c	correspondence address				
Period for Reply	VIO OET TO EVOIDE A MONTH	CO) EDOM				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be tin ly within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from e. cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 17 N	lovember 2003.					
2a) This action is FINAL . 2b) ☑ This	s action is non-final.					
·	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 1-25 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) Claim(s) is/are allowed. 6) Claim(s) 1-25 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/o	iwn from consideration.					
Application Papers						
9) The specification is objected to by the Examination 10) The drawing(s) filed on 17 November 2003 is selected to a specific property of the Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Examination is objected.	are: a) \boxtimes accepted or b) \square objected drawing(s) be held in abeyance. Section is required if the drawing(s) is ob-	e 37 CFR 1.85(a). njected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal I 6) Other:					

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 12 and 25 are rejected under 35 U.S.C. 102(b) as being anticipated by Yanagisawa et al. (U.S. Patent 6,147,915).

Regarding claims 1 and 25, Yanagisawa discloses an apparatus (figure 1 and related text) comprising:

a first transistor (51) having a first control terminal (gate electrode) coupled to receive a first dynamic data signal (input to inverter 41), wherein the first transistor is coupled to a first node (terminal 53-0) and is configured to drive a first state (logic 0) on the first node responsive to an assertion of the first dynamic data signal;

a second transistor (50) coupled to the first node and having a second control terminal (gate electrode), the second transistor configured to drive a second state (logic high) on the first node responsive to a signal on the second control terminal; and

a circuit (41) coupled to generate the signal on the second control terminal and coupled to receive a second dynamic data signal (input to inverter 42), the second dynamic signal being a complement of the first dynamic signal, wherein the circuit is configured to activate the second transistor responsive to an assertion of the second dynamic data signal (column 2, lines 24-46).

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Regarding claim 12, Yanagisawa shows in figure 1, the circuit comprises an inverter (41).

Double Patenting

3. A rejection based on double patenting of the "same invention" type finds its support in the language of 35 U.S.C. 101 which states that "whoever invents or discovers any new and useful process ... may obtain <u>a</u> patent therefor ..." (Emphasis added). Thus, the term "same invention," in this context, means an invention drawn to identical subject matter. See *Miller v. Eagle Mfg. Co.*, 151 U.S. 186 (1894); *In re Ockert*, 245 F.2d 467, 114 USPQ 330 (CCPA 1957); and *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970).

A statutory type (35 U.S.C. 101) double patenting rejection can be overcome by canceling or amending the conflicting claims so they are no longer coextensive in scope. The filing of a terminal disclaimer <u>cannot</u> overcome a double patenting rejection based upon 35 U.S.C. 101.

- 4. Claims 13-24 are rejected under 35 U.S.C. 101 as claiming the same invention as that of claims 13-24 of prior U.S. Patent No. 6,674,671. This is a double patenting rejection.
- 5. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970);and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

6. Claims 1-12 and 25 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-12 and 25 of

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(Bd App. 1969).

U.S. Patent No. 6,674,671. Although the conflicting claims are not identical, they are not patentably distinct from each other because some limitations of the Patent are omitted in the instant claims. In claims 1 and 25 of the instant application, "a source circuit" is omitted from the Patent's claims 1 and 25. A source circuit generating a complementary data signals is known in the art (element 80 in figure 1 of Yanagisawa). Clearly, the Applicant is attempting to obtain broader coverage in the claim of the application. However, it is well settled that the omission of elements and its function is an obvious expedient if the remaining elements perform the same function as before. In

Conclusion

re Karlson, 136 USPQ 184 (CCPA 1963). Also note Ex parte Rainu, 168 USPQ 375

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Tipon (U.S. Patent 5,585,740), Jeon (U.S. Patent 5,701,072), Matsumoto (U.S. Patent 6,650,582) disclose bus data drivers in memory devices.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Son L. Mai whose telephone number is 571-272-1786. The examiner can normally be reached on 8am to 6pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 571-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

05-01-04

Son L. Mai Primary Examiner Art Unit 2818